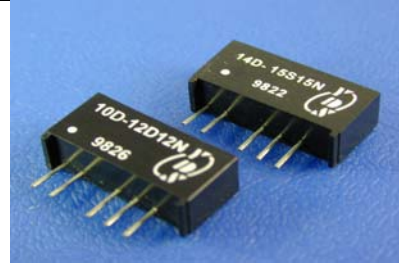


TSS 10D/14D SERIES 1000 VDC SINGLE AND DUAL OUTPUT 1 WATT DC-DC CONVERTER

FEATURES

- 7 PIN SIP PACKAGE.
- 1000VDC ISOLATION.
- UNREGULATED OUTPUT TYPES.
- UL 94V-0 PACKAGE MATERIAL.



GENERAL SPECIFICATIONS

- OUTPUT RIPPPL(20MHz Band limited)
INPUT 3.3V AND 48V TYP. : 150mVp-p MAX
INPUT 5V,12V AND 24V TYP. : 100mVp-p MAX
- ISOLATION RESISTANCE :1000MΩ MIN @500VDC
- SWITHINGFREQUENCY :100KHz TYP.
- OPERATING TEMPERATURE :0 °C TO 70°C
- EFFICIENCY :70% TYP
- ISOLATION VOLTAGE :1000 VDC

ELECTRICAL SPECIFICATIONS

- INPUT VOLTAGE RANGE :±10%
- OUTPUT VOLTAGE TOLERANCE :±5%
- LINE REGULATION(10% TO 100% F.L.) :1.2%/1% OF Vin
- LOAD REGULATION(10% TO 100% F.L.) :15% MAX
3.3V AND 5V OUTPUT TYPES :10% MAX
9V,12V AND 15V OUTPUT TYPES :10% MAX

3.3V,5V,12V,24V INPUT TYPES

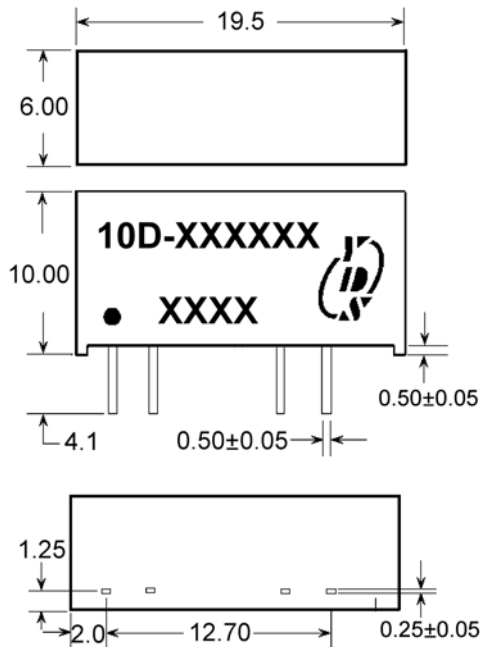
PART NUMBER	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (mA)	EFFICIENCY (% TYP.)	PACKAGE STYLE	PART NUMBER	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (mA)	EFFICIENCY (% TYP.)	PACKAGE STYLE
10D-XXS05N	5	200	70	1	10D-48S05N2	5	200	70	2
10D-XXS09N	9	112	75	1	10D-48S09N2	9	112	75	2
10D-XXS12N	12	84	75	1	10D-48S12N2	12	84	75	2
10D-XXS15N	15	67	75	1	10D-48S15N2	15	67	75	2
14D-XXD05N	±5	±100	70	1	14D-48D05N2	±5	±100	70	2
14D-XXD09N	±9	±56	75	1	14D-48D09N2	±9	±56	75	2
14D-XXD12N	±12	±42	75	1	14D-48D12N2	±12	±42	75	2
14D-XXD15N	±15	±34	75	1	14D-48D15N2	±15	±34	75	2

48V INPUT TYPES

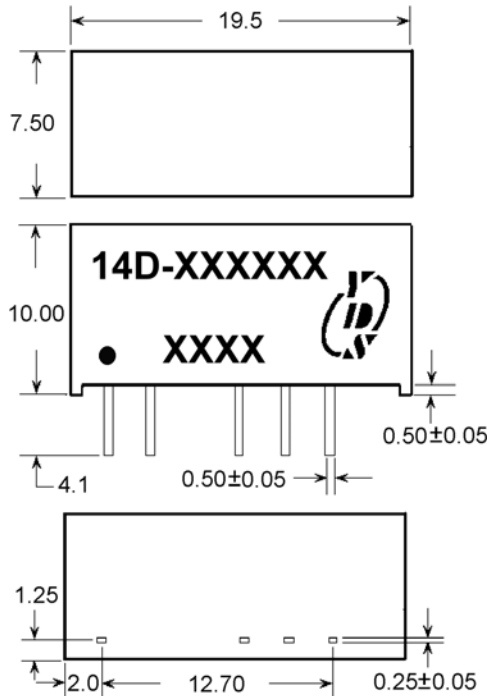
MARKINGS AND DIMENSIONS

PIN CONNECTION

1.



2.



PIN	SINGLE		DUAL OUTPUT
	10D	14D	
1	+IN	+IN	+INPUT
2	-IN	-IN	-INPUT
4	NC	-OUT	-OUTPUT
5	-OUT	NC	COMMON
6	+OUT	+OUT	+OUTPUT

*NC PIN NOT PRESENTED ON SINGLE OUTPUT MODELS

Dimensions:inches/mm

Unless otherwise specified, all tolerances are ±.005/±0.13